AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A nitride compound semiconductor element comprising: a sapphire substrate;

a first single crystalline layer of AIN AIN formed on said sapphire substrate;

a second single crystalline layer formed on said first single crystalline layer, said second single crystalline layer being made of $Al_xGa_{1-x}N$ (0.8 0.85 $\leq x \leq$ 0.97 0.95) and having a thickness of equal to or more than 0.3 0.7 μ m and equal to or less than 6 3 μ m; and

a device structure section of a nitride compound semiconductor formed on said second single crystalline layer.

Claim 2 (Canceled)

Claim 3 (Original): The nitride compound semiconductor element according to claim 1, wherein said first single crystalline layer has a thickness of equal to or more than 10 nm and equal to or less than 50 nm.

Claim 4 (Original): The nitride compound semiconductor element according to claim 1, wherein said first single crystalline layer is doped with carbon having a concentration of equal to or more than 3×10^{18} cm⁻³ and equal to or less than 1×10^{20} cm⁻³.

Claim 5 (Original): The nitride compound semiconductor element according to claim 1, wherein said sapphire substrate either has no warp or is warped concavely toward said device structure section.

6. (Currently Amended) The nitride compound semiconductor element according to claim 1, further comprising:

(1)

a single crystalline AIN AIN protective layer formed directly on said second single crystalline layer for preventing Ga atoms from evaporating from said second single crystalline layer to thereby protect said second single crystalline layer, said AIN AIN protective layer having a thickness of equal to or more than 1 nm and equal to or less than 10 nm.

7. (Currently Amended) The nitride compound semiconductor element according to claim 4, further comprising:

an AIN AIN layer formed between said first single crystalline layer and said second single crystalline layer, said AIN AIN layer containing no impurity or containing impurity having a concentration of less than 3×10^{18} cm⁻³.

Claim 8 (Original): The nitride compound semiconductor element according to claim 1, wherein said nitride compound semiconductor element comprises a semiconductor laser and wherein said device structure section comprises:

a first conductive-type semiconductor layer;

an active layer formed on said first conductive-type semiconductor layer for emitting light by current injection; and

a second conductive-type semiconductor layer formed on said active layer.

Claim 9 (Original): The nitride compound semiconductor element according to claim 8, wherein said active layer contains a well layer made of $Ga_{1-z}In_zN$ (0.15 $\leq z \leq$ 0.3).

Claim 10 (Original): The nitride compound semiconductor element according to claim 1, further comprising:

a lattice modification layer formed between said second single crystalline layer and said device structure section, said lattice modification layer being made of Al_yGa_{1-y}N (0.25 \leq y \leq 0.75) and having a thickness of equal to or more than 0.3 μ m and equal to or less than 3 μ m.

Claim 11 (Original): The nitride compound semiconductor element according to claim 10, wherein said nitride semiconductor comprises an optical switch or a field effect transistor.

Claim 12 (Original): The nitride compound semiconductor element according to claim 10, wherein said device structure section has a heterojunction of an AlN layer and a GaN layer.

Claim 13 (Original): The nitride compound semiconductor element according to claim 12, wherein said nitride semiconductor comprises an optical switch.

Claims 14-20 (Canceled)

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21. (New) A method of making a nitride compound semiconductor element, the method comprising

providing a sapphire substrate;

depositing on the sapphire substrate a first single crystalline layer of AlN;

depositing on the first single crystalline layer a second single crystalline layer of $Al_xGa_{1-x}N\ (0.85 \le x \le 0.95)\ having\ a\ thickness\ of\ equal\ to\ or\ more\ than\ 0.7\ \mu m\ and\ equal\ to$ or less than 3 μm ;

depositing on the second single crystalline layer a device structure section of a nitride compound semiconductor; and

producing the semiconductor element of claim 1.